



Pin No.	Name	I/O	Function
78	A12	I	Address signals from $\mu$ P.
79	A11		
80	A10		
1	A9		
2	A8		
3	A7		
4	A6		
5	A5		
6	A4		
7	A3		
8	A2		
9	A1		
10	A0		
21	D7	I/O	Data bus signals with $\mu$ P.
22	D6		
23	D5		
24	D4		
25	D3		
26	D2		
27	D1		
28	D0		
77	CSB	I	Chip select signal from $\mu$ P.
13	RDB	I	Read signal from $\mu$ P.
14	WRB	I	Write signal from $\mu$ P.
44	RAMAD7	I/O	When connected to a pseudo SRAM, these pins provide multiplex signals of the low order address/data to the SRAM, and when connected to an MROM, these pins provide data input signal from the MROM.
45	RAMAD6		
46	RAMAD5		
47	RAMAD4		
48	RAMAD3		
49	RAMAD2		
50	RAMAD1	I/O	When connected to an SRAM, these pins also provide data bus signals to the SRAM.
51	RAMAD0		
53	RAMA14	O	High order address signals of the SRAM & MROM.
54	RAMA13		
55	RAMA12		
56	RAMA11		
57	RAMA10		
58	RAMA9		
59	RAMA8		
65	RAMA7	O	Low address signals of the SRAM & MROM.
66	RAMA6		
67	RAMA5		
68	RAMA4		
73	RAMA3		
74	RAMA2		

Pin No.	Name	I/O	Function
75	RAMA1	O	Low address signals of the SRAM & MROM.
76	RAMA0		
42	RAMAX	O	LSB address signal of the MROM.
61	RAMC2B	O	High order 32k byte SRAM & MROM select signal.
60	RAMC1B	O	Low order 32k byte SRAM & MROM select signal.
63	RAMWEB	O	Signal to write data to the pseudo SRAM or SRAM.
62	RAMOEB	O	Signal to read data from the pseudo SRAM, SRAM or MROM.
31	DAC7	O	Multiplex signals of "R" and "L" data output to the parallel DAC.
32	DAC6		
33	DAC5		
34	DAC4		
37	DAC3	O	DAC7-3 "L" data sample/hold signal.
29	SHL		
30	SHR	O	DAC7-3 "R" data sample/hold signal.
39	DAC7R	O	Signal obtained by sampling and holding the DAC7 output at SHR.
38	DAC7L	O	Signal obtained by sampling and holding the DAC7 output at SHL.
41	WCLK1	O	Word clock signal output to the serial DAC.
40	LRCLK	O	LR clock signal output to the serial DAC.
36	DATA	O	Digital audio data signal output to the serial DAC.
35	BCLK	O	Bit clock signal output to the serial DAC.
64	RESETB	I	Reset signal.
70	XIN	I	An external crystal oscillator is connected. A clock signal is input to XIN directly.
71	XOUT	O	
16	TEST1	I	Test signal inputs. Normally, fixed at "L". However, TEST2 is fixed at "H" when an MROM or SRAM is used.
17	TEST2		
18	TEST3		
12	VCC	-	Power supply pins.
19			
43			
43	GND	-	Ground pins.
69			
15			
52	GND	-	Ground pins.
72			