



Pin No.	Name	I/O	Function	Pin No.	Name	I/O	Function
78	A12	I	Address signals from μ P.	75	RAMA1	O	Low address signals of the SRAM & MROM.
79	A11			76	RAMA0	O	
80	A10			42	RAMAX	O	LSB address signal of the MROM.
1	A9			61	RAMC2B	O	High order 32k byte SRAM & MROM select signal.
2	A8			60	RAMC1B	O	Low order 32k byte SRAM & MROM select signal.
3	A7			63	RAMWEB	O	Signal to write data to the pseudo SRAM or SRAM.
4	A6			62	RAMOEB	O	Signal to read data from the pseudo SRAM, SRAM or MROM.
5	A5			31	DAC7	O	Multiplex signals of "R" and "L" data output to the parallel DAC.
6	A4			32	DAC6		
7	A3			33	DAC5		
8	A2	34	DAC4				
9	A1	37	DAC3	29	SHL	O	DAC7-3 "L" data sample/hold signal.
10	A0	30	SHR	O	DAC7-3 "R" data sample/hold signal.		
21	D7	I/O	Data bus signals with μ P.	39	DAC7R	O	Signal obtained by sampling and holding the DAC7 output at SHR.
22	D6			38	DAC7L	O	Signal obtained by sampling and holding the DAC7 output at SHL.
23	D5			41	WCLK1	O	Word clock signal output to the serial DAC.
24	D4			40	LRCLK	O	LR clock signal output to the serial DAC.
25	D3			36	DATA	O	Digital audio data signal output to the serial DAC.
26	D2			35	BCLK	O	Bit clock signal output to the serial DAC.
27	D1			64	RESETB	I	Reset signal.
28	D0			70	XIN	I	An external crystal oscillator is connected. A clock signal is input to XIN directly.
77	CSB	I	Chip select signal from μ P.	71	XOUT	O	
13	RDB	I	Read signal from μ P.	16	TEST1	I	Test signal inputs. Normally, fixed at "L". However, TEST2 is fixed at "H" when an MROM or SRAM is used.
14	WRB	I	Write signal from μ P.	17	TEST2		
44	RAMAD7	I/O	When connected to a pseudo SRAM, these pins provide multiplex signals of the low order address/data to the SRAM, and when connected to an MROM, these pins provide data input signal from the MROM. When connected to an SRAM, these pins also provide data bus signals to the SRAM.	18	TEST3		
45	RAMAD6			12	VCC	-	Power supply pins.
46	RAMAD5			19			
47	RAMAD4			43			
48	RAMAD3			69			
49	RAMAD2			15	GND	-	Ground pins.
50	RAMAD1	52					
51	RAMAD0	72					
53	RAMA14	O	High order address signals of the SRAM & MROM.	16			
54	RAMA13						
55	RAMA12						
56	RAMA11						
57	RAMA10						
58	RAMA9						
59	RAMA8	O	Low address signals of the SRAM & MROM.	12	VCC	-	Power supply pins.
65	RAMA7						
66	RAMA6						
67	RAMA5						
68	RAMA4						
73	RAMA3						
74	RAMA2	GND	-	Ground pins.			